

**REQUEST
FOR
CONTINUED EXAMINATION (RCE)
TRANSMITTAL**

Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000, provides for continued examination of an utility or plant application filed on or after June 8, 1995.
See The American Inventors Protection Act of 1999 (AIPA).

<i>Application Number</i>	10/643,585
<i>Filing Date</i>	August 18, 2003
<i>First Named Inventor</i>	Steven L. Scott
<i>Group Art Unit</i>	2186
<i>Examiner Name</i>	Sheng Tsai
<i>Attorney Docket Number</i>	1376.700US1
<i>Customer No.</i>	21186

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application entitled LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER

1. Submission required under 37 C.F.R. § 1.114:

- Information Disclosure Statement (2 pages), Form 1449 (1 page), and copies of cited documents (10).
- Submission of Formal Drawings (1 pg).
- Replacement Formal Drawings (Figs. 1-12; seven sheets).

2. Fees

- Authorization to charge deposit account 19-0743 in the amount of \$810.00 to pay the RCE filing fee required under 37 C.F.R. § 1.17(e).
- The Commissioner is hereby authorized to charge any additional fees or credit overpayment to Deposit Account No. 19-0743.**

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.

By: Thomas F. Brennan
Thomas F. Brennan
Reg. No. 35,075

CERTIFICATE UNDER 37 C.F.R. 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 8 day of July, 2008.

CANDIS BUENDING

Name

Signature

Candis Buending